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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Mitsuaki Hori et al.**

Group Art Unit: **2826**

Serial No.: **10/662,384**

Examiner: **SEFER, Ahmed N.**

Filed: **September 16, 2003**

Confirmation No.: **2168**

For: **SEMICONDUCTOR DEVICE, MANUFACTURE AND EVALUATION
METHODS FOR SEMICONDUCTOR DEVICE, AND PROCESS
CONDITION EVALUATION METHOD**

Attorney Docket No.: **031111**

Customer Number: **38834**

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

June 3, 2004

Sir:

This paper is submitted in response to the Official Action dated May 4, 2004.

In the Action, restriction is required between Group (I), Claims 1-3 and 11-13; and Group (II), Claims 4-10.

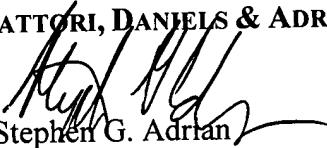
Applicants hereby elect the subject matter of Group (II), Claims 4-10 for prosecution in this application. This election is made without traverse, and it is understood that Applicant's rights to the filing of a divisional application directed to the non-elected subject matter under 35 U.S.C. §120 and 35 U.S.C. §121 are retained.

Response to Restriction Requirement
Attorney Docket No. 03111
Serial No. 10/662,384

If this paper is not timely filed, Applicants respectfully petition for an appropriate extension of time. The fees for such an extension or any other fees that may be due with respect to this paper may be charged to Deposit Account No. 50-2866.

Respectfully submitted,

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